

**This Page Is Inserted by IFW Operations
and is not a part of the Official Record**

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- **BLACK BORDERS**
- **TEXT CUT OFF AT TOP, BOTTOM OR SIDES**
- **FADED TEXT**
- **ILLEGIBLE TEXT**
- **SKEWED/SLANTED IMAGES**
- **COLORED PHOTOS**
- **BLACK OR VERY BLACK AND WHITE DARK PHOTOS**
- **GRAY SCALE DOCUMENTS**

IMAGES ARE BEST AVAILABLE COPY.

**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problem Mailbox.**

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
19 July 2001 (19.07.2001)

PCT

(10) International Publication Number
WO 01/52319 A1

(51) International Patent Classification⁷: H01L 21/66

Teaberry Drive, Austin, TX 78745 (US). CHRISTIAN,
Craig, W.; 12513 Eagle Nest Drive, Buda, TX 78610 (US).

(21) International Application Number: PCT/US00/24411

(22) International Filing Date:
6 September 2000 (06.09.2000)

(74) Agent: DRAKE, Paul, S.; Advanced Micro Devices, Inc.,
5204 East Ben White Boulevard, M/S 562, Austin, TX
78741 (US).

(25) Filing Language: English

(81) Designated States (*national*): JP, KR.

(26) Publication Language: English

(84) Designated States (*regional*): European patent (AT, BE,
CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC,
NL, PT, SE).

(30) Priority Data:
09/479,180 7 January 2000 (07.01.2000) US

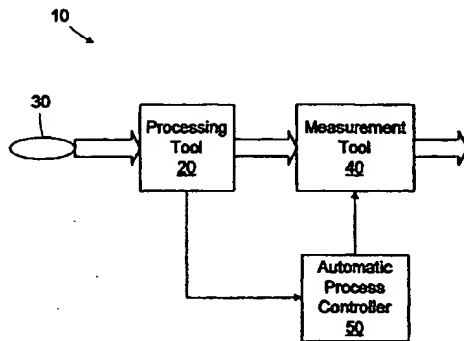
(71) Applicant: ADVANCED MICRO DEVICES, INC.
[US/US]; One AMD Place, Mail Stop 68, Sunnyvale, CA
94088-3453 (US).

Published:
— with international search report

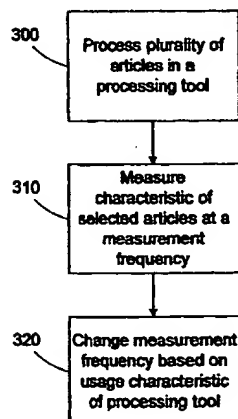
(72) Inventors: DAVIS, Bradley, M.; 2611 Bee Caves Road
#242, Austin, TX 78748 (US). EVANS, Allen, L.; 7214

For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.

(54) Title: METHOD AND APPARATUS FOR DETERMINING MEASUREMENT FREQUENCY BASED ON HARDWARE
AGE AND USAGE



(57) Abstract: A processing line (10) includes a processing tool (20), a measurement tool (40), and an automatic process controller (50). The processing tool (20) is adapted to process articles (30). The measurement tool (40) is adapted to measure a characteristic of selected articles (30) at a measurement frequency. The automatic process controller (50) is adapted to change the measurement frequency based on a usage characteristic of the processing tool (20). A method for monitoring a processing tool (20) includes processing a plurality of articles (30) in the processing tool (20); measuring a characteristic of selected articles (30) at a measurement frequency; and changing the measurement frequency based on a usage characteristic of the processing tool (20).



WO 01/52319 A1

METHOD AND APPARATUS FOR DETERMINING MEASUREMENT FREQUENCY BASED ON HARDWARE AGE AND USAGE

TECHNICAL FIELD

5 This invention relates generally to semiconductor device manufacturing, and, more particularly, to a method and apparatus for determining measurement frequency based on hardware age and usage.

BACKGROUND ART

10 The manufacture of most devices, such as semiconductor devices, requires a number of discrete processing steps to create the device. For the example of semiconductor devices, a number of discrete steps are needed to produce a packaged semiconductor circuit device from raw semiconductor material. The starting substrate is usually a slice of single crystal silicon referred to as a wafer. Circuits of a particular type are fabricated together in batches of wafers called "lots" or "runs". The fabrication process creates regular arrays of a circuit on the wafers of a lot. During processing, the individual wafers in a lot may go through individual processing steps one at a time or as a batch. At the completion of wafer processing, the wafers are tested to determine circuit functionality. Later the wafers are sliced, the functioning devices are packaged, and further testing occurs prior to use by the customer.

Data gathered during the course of wafer processing is used to diagnose yield problems and forms the basis of yield improvement efforts. Such performance measurements include defect count measurements, thickness measurements (i.e., indicative of deposition rate), and resistivity measurements, for example.

20 The number of defects is often predictable as a function of tool hardware life. The resistivity and deposition rate also change with hardware life, and/or maintenance history. For much deposition equipment, defects are more probable when the tool is first put together and at the end of its life. In a sputtering deposition tool, the source of metal to be sputtered is referred to as a target. The target is depleted as sputtering is conducted, and, thus, the target is changed frequently. The resistivity and/or deposition rate of the layer being deposited change slowly after a target change and after a wet clean.

25 In a chemical vapor deposition (CVD) tool, reactive gases are introduced into the processing chamber through a gas supply header, commonly referred to as a showerhead. Over time, process materials and/or byproducts collect on the showerhead, eventually leading to degraded performance. As the showerhead becomes obstructed, the deposition rate of the CVD tool decreases and becomes more erratic. The gas supplied to the showerhead often passes through an in-line filter. Over time, the filter may become obstructed, thus reducing the amount of reactive gases it passes. This reduction could also eventually cause a reduction in the deposition rate of the tool.

30 Current semiconductor processing techniques typically take performance measurements at a fixed rate (e.g., every fourth lot processed in a tool) or by pre-assigning a fixed percentage of lots for measurement. Because lots are not typically processed in a particular order, the percentage technique sometimes results in periods where multiple lots are measured consecutively, followed by periods where no lots are measured.

35 To address the stability problem identified above, preventative maintenance tasks, such as cleanings, tool refurbishment, showerhead replacement, in-line filter replacement, and target replacement, are scheduled at intervals that are less than the expected time where the likelihood of instabilities increases. These intervals, by nature, are conservative, and may result in the performance of the preventative maintenance task before it is actually necessary. Also, due to the limitations of the performance measurement intervals described above,

degradation of the tool prior to the end of the preventative maintenance interval may not be readily identified. As a result defective wafers could be manufactured, necessitating costly re-work or scrapping of the wafers.

The present invention is directed to resolving one or all of the problems mentioned above.

DISCLOSURE OF INVENTION

5 One aspect of the present invention is seen in a processing line including a processing tool, a measurement tool, and an automatic process controller. The processing tool is adapted to process articles. The measurement tool is adapted to measure a characteristic of selected articles at a measurement frequency. The automatic process controller is adapted to change the measurement frequency based on a usage characteristic of the processing tool.

10 Another aspect of the present invention is seen in a method for monitoring a processing tool. The method includes processing a plurality of articles in the processing tool; measuring a characteristic of selected articles at a measurement frequency; and changing the measurement frequency based on a usage characteristic of the processing tool.

BRIEF DESCRIPTION OF THE DRAWINGS

15 The invention may be understood by reference to the following description taken in conjunction with the accompanying drawings, in which like reference numerals identify like elements, and in which:

Figure 1 is a simplified block diagram of a processing line in accordance with the present invention;

Figure 2 is a defect count curve for a deposition tool of Figure 1 following a cleaning or refurbishment;

Figure 3 is a deposition rate curve for the deposition tool of Figure 1 following a cleaning or part replacement; and

20 Figure 4 is a flow diagram of a method for monitoring a processing tool in accordance with the present invention.

While the invention is susceptible to various modifications and alternative forms, specific embodiments thereof have been shown by way of example in the drawings and are herein described in detail. It should be understood, however, that the description herein of specific embodiments is not intended to limit the invention to the particular forms disclosed, but on the contrary, the intention is to cover all modifications, equivalents, and alternatives falling within the spirit and scope of the invention as defined by the appended claims.

MODE(S) FOR CARRYING OUT THE INVENTION

25 Illustrative embodiments of the invention are described below. In the interest of clarity, not all features of an actual implementation are described in this specification. It will of course be appreciated that in the development of any such actual embodiment, numerous implementation-specific decisions must be made to achieve the developers' specific goals, such as compliance with system-related and business-related constraints, which will vary from one implementation to another. Moreover, it will be appreciated that such a development effort might be complex and time-consuming, but would nevertheless be a routine undertaking for those of ordinary skill in the art having the benefit of this disclosure.

35 Turning now to the figures, and, in particular, to Figure 1, a block diagram of a processing line 10 in accordance with the present invention is provided. The processing line 10 includes a processing tool 20 for processing semiconductor wafers 30 and a measurement tool 40 for taking performance measurements on some of the wafers 30 processed by the processing tool 20 to measure the efficacy of the process implemented by the processing tool 20. An automatic process controller 50 interfaces with the measurement tool 40 and the processing tool 20, at least in part, to determine the measurement frequency used by the measurement tool 40 based on

characteristics of the processing tool 20. The measurement tool 40 may be remote from the processing tool 20 as shown, or alternatively, the measurement tool 40 may be integrated with the processing tool 20 (*i.e., in situ*)

The particular process performed by the processing tool 20 and the particular performance measurement taken by the measurement tool 40 may vary widely. Various illustrative examples will be described in greater detail hereinafter; however, application of the present invention is not limited to the particulars described in such examples. Rather, the instant invention is applicable to a wide variety of processing tools 20 related or not to semiconductor processing, and the performance measurement may be selected from a wide range of performance measurements applicable to the particular product being processed or the particular process being performed.

The automatic process controller 50 adjusts the measurement frequency of the measurement tool 40 based on usage characteristics of the processing tool 20. Exemplary usage characteristics include the time elapsed since a preventative maintenance task has been performed (*e.g., cleaning*) and the time elapsed since a particular part in the processing tool 20 has been replaced (*e.g., target, shower head, in-line filter*). The performance measurement taken by the measurement tool 40 to gauge the efficacy of the processing tool 20 may include parameters such as, but not limited to, process layer thickness, defect count, or resistivity.

In the illustrated embodiment, the automatic process controller 50 comprises a computer executing application software adapted to perform the functions described. The automatic process controller 50 may be coupled to other computers (not shown) over a computer network (not shown). Maintenance on the processing tool 20 may be tracked by a factory control system accessible to the automatic process controller 50. Alternatively, the automatic process controller 50 may receive usage characteristics via manual input from the operator of the processing tool 20.

Based on the usage characteristics derived or received by the automatic process controller 50, the measurement frequency of the measurement tool 40 is increased or decreased, based, at least in part, on the expected stability of the processing tool 20. During times of expected instability, the automatic process controller 50 increases the measurement frequency of the measurement tool 40, and during times of expected greater stability, the measurement frequency is decreased. It will be appreciated that intermediate frequencies may be used during transitions from expected periods of instability to periods of greater stability.

Exemplary period of increased instability include immediately following a cleaning operation; immediately following a target, showerhead, or in-line filter replacement; near the expected end-of-life of a target, showerhead, or filter; near the anticipated time that a cleaning should be performed, etc. Periods of greater stability may be defined as those periods other than those described above as being unstable, otherwise referred to as normal or steady-state operating conditions.

Figure 2 illustrates a defect count curve for a deposition tool following a cleaning or refurbishment. In an initial period 100, the defect count may be high. During the initial period 100, the measurement frequency of the measurement tool 40 is increased as compared to its default value. A steady-state period 110 follows the initial period 100, and the measurement frequency of the measurement tool 40 is decreased to the default value. Finally, as the age of the hardware increases, an end-of-life period 120 is entered where the defect count again may increase. During the end-of-life period 120, the measurement frequency of the measurement tool 40 is again increased. The transition times between the steady-state period 110 and the initial and end-of-life periods 100, 120 are not completely predictable. The determination of the times to transition from one period 100, 110, 120 to another may be based on predetermined time frames or, alternatively based on the performance measurements

themselves. The particular frequencies for the initial period 100 and the end-of-life period 120 may not necessarily be the same.

For example, if during the initial period 100, where the measurement tool 40 is measuring at an increased frequency, and the automatic process controller 50 detects a sustained decrease in the defect count, the automatic process controller 50 directs the measurement tool 40 to transition to the default frequency. Likewise, if the measurement tool 40 is measuring at the default frequency, and the automatic process controller 50 detects a sustained increase in the defect count, the automatic process controller 50 directs the measurement tool 40 to transition to the increased frequency.

Figure 3 illustrates a deposition rate curve for a deposition tool following a cleaning or showerhead replacement. In an initial period 200, the deposition rate is generally stable. During the initial period 200, the measurement frequency of the measurement tool 40 is set by the automatic process controller 50 to the default frequency. Over time, as material builds up on the processing tool 20, the target is depleted, or the in-line filter becomes obstructed, the deposition rate decreases. The period marked by a marked decrease in deposition rate is the end-of-life period 210. During the end-of-life period 120, the measurement frequency of the automatic process controller increases the measurement frequency of the measurement tool 40 to the increased value.

The transition to the increased frequency could be used to trigger an alert or message to an operator of the processing tool 20 to evaluate the conditions and possibly schedule a maintenance task. Using such an approach, the maintenance interval could be effectively lengthened when the transition to the higher frequency occurs at a point in time later than the expected time for entering the end-of-life interval 120, 210. This approach identifies both early degraded conditions as well as conditions more favorable than predicted where maintenance intervals can be extended.

The specific frequencies for the default frequency and the increased frequency are application dependent. For example, the default frequency may be every N lots, every shift, every month, etc. The increased frequency may be some multiple (e.g., two or three times) the default frequency.

It is contemplated that the automatic process controller 50 may be configured to adaptively change the frequency and or transition times based on the actual history of the measurement tool 40. For example, the automatic process controller 50 may attempt maintain a constant ratio between the percent failed and the percent measured. That is, during periods with a low number of failures the frequency may be adaptively decreased. Likewise, during periods with higher rates, the frequency may be adaptively increased. In such an embodiment, there may not be distinct intervals, but rather a continuum of adaptively changing measurement frequencies based on the current failure rate.

The automatic process controller 50 may be adapted to change the measurement frequency of the measurement tool 40 to identify a number of degraded conditions. For example, the automatic process controller 50 may increase the measurement frequency around times of expected cleanings, target replacement, shower-head replacement, and in-line filter replacement. These intervals may or may not overlap.

Turning now to Figure 4, a flow diagram of a method for monitoring a processing tool 20 is provided. In block 300, a plurality of articles are processed in the processing tool 20. In block 310, a characteristic of selected articles is measured at a measurement frequency. The measurement frequency is changed based on a usage characteristic of the processing tool 20, in block 320.

The particular embodiments disclosed above are illustrative only, as the invention may be modified and practiced in different but equivalent manners apparent to those skilled in the art having the benefit of the teachings

WO 01/52519

herein. Furthermore, no limitations are intended to the details of construction or design herein shown, other than as described in the claims below. It is therefore evident that the particular embodiments disclosed above may be altered or modified and all such variations are considered within the scope and spirit of the invention. Accordingly, the protection sought herein is as set forth in the claims below.

PC 170300/244

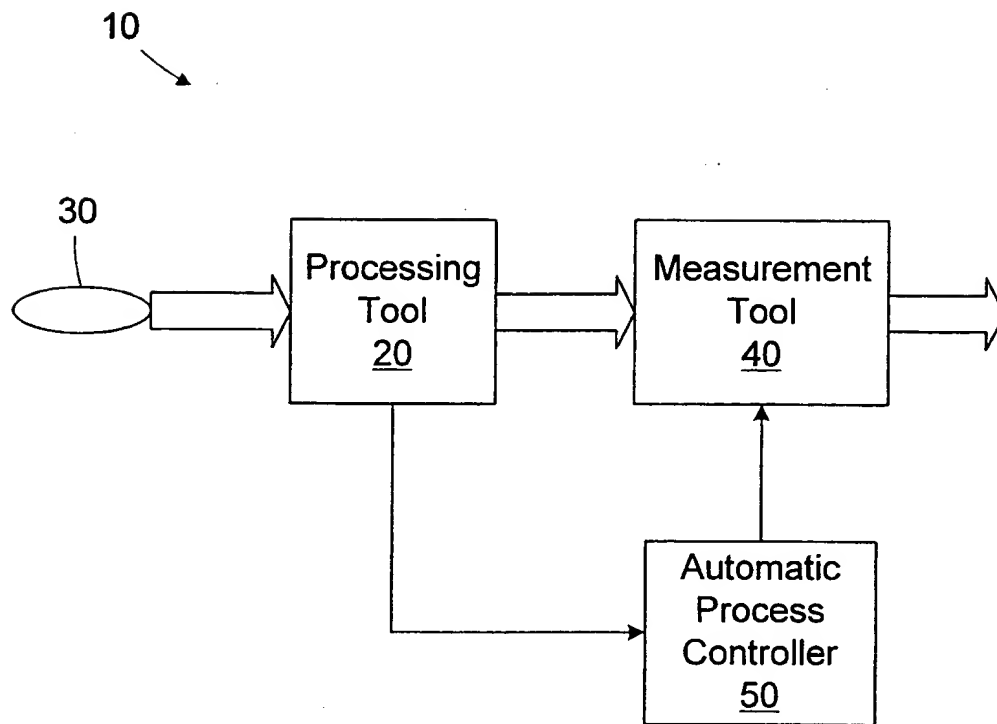
CLAIMS

1. A processing line (10), comprising:
a processing tool (20) adapted to process articles (30);
a measurement tool (40) adapted to measure a characteristic of selected articles (30) at a measurement
5 frequency; and
an automatic process controller (50) adapted to change the measurement frequency based on a usage
characteristic of the processing tool (20).
2. The processing line (10) of claim 1, wherein the automatic process controller (50) is adapted to
10 set the measurement frequency at a first frequency during a first time period and set the measurement frequency at
a second frequency during a second time period.
3. The processing line (10) of claim 2, wherein the automatic process controller (50) is adapted to
15 transition from the first time period to the second time period based on a least one of a predetermined time and the
characteristic measured by the measuring tool (40).
4. The processing line (10) of claim 1, wherein the articles (30) comprise semiconductor wafers, the
processing tool (20) is adapted to form a layer of material on the wafers, and the characteristic measured by the
20 measurement tool (40) comprises at least one of a thickness of the layer of material, a resistivity of the layer of
material, and a defect count associated with the layer of material.
5. The processing line (10) of claim 1, wherein the usage characteristic comprises at least one of a
period of time elapsed since a preventative maintenance task has been performed on the processing tool (20) and a
period of time elapsed since a particular part in the processing tool (20) has been replaced.
25
6. A method for monitoring a processing tool (20), comprising:
processing a plurality of articles (30) in the processing tool (20);
measuring a characteristic of selected articles (30) at a measurement frequency; and
changing the measurement frequency based on a usage characteristic of the processing tool (20).
30
7. The method of claim 6, wherein changing the measurement frequency includes setting the
measurement frequency at a first frequency during a first time period, and setting the measurement frequency at a
second frequency during a second time period.
8. The method of claim 7, wherein setting the measurement frequency at a second frequency
35 includes setting the measurement frequency at a second frequency based on at least one of a predetermined period
of time and the characteristic measured.
9. The method of claim 6, wherein processing the plurality of articles (30) comprises processing a
40 plurality of semiconductor wafers, the processing tool (20) is adapted to form a layer of material on the wafers, and

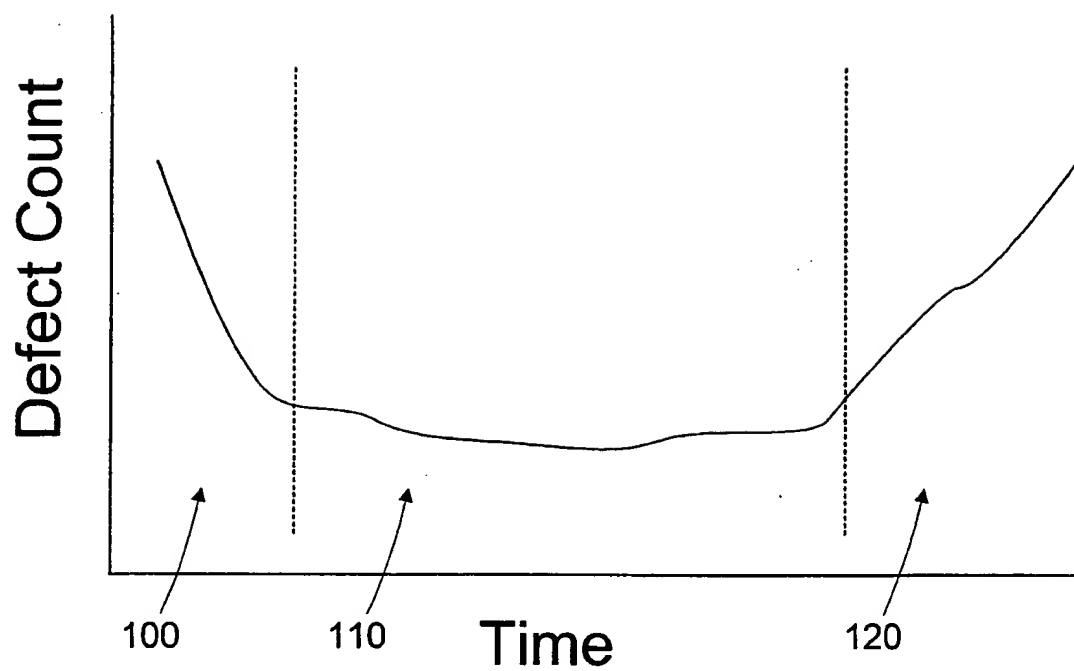
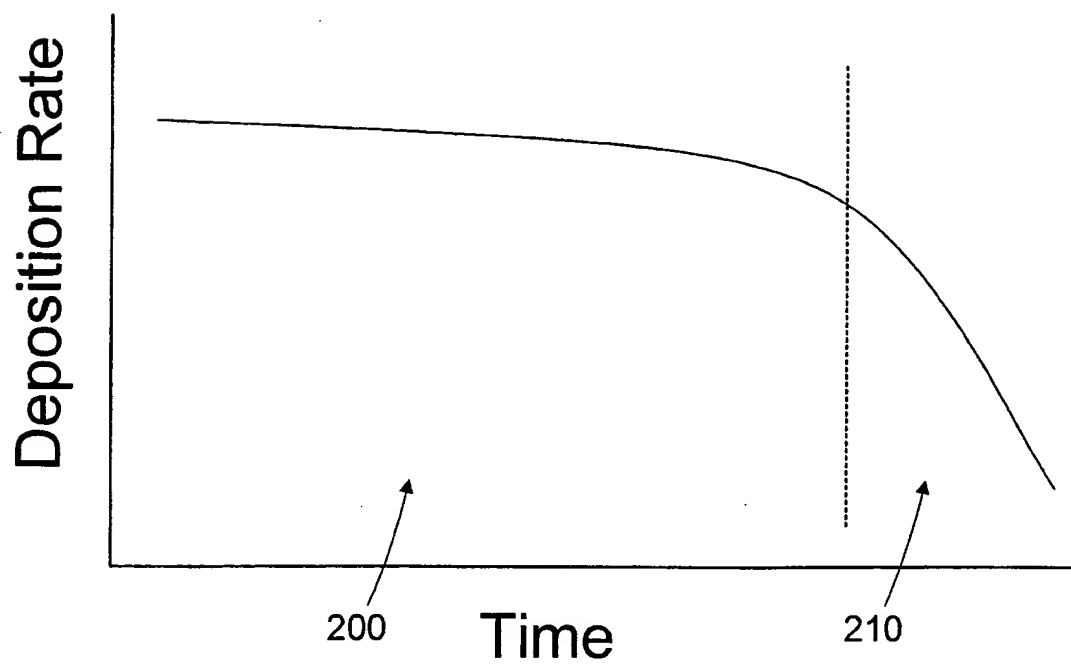
measuring the characteristic comprises measuring at least one of a thickness of the layer of material, a resistivity of the layer of material, and a defect count associated with the layer of material.

- 5 10. The method of claim 6, wherein changing the measurement frequency based on the usage characteristic of the processing tool (20) comprises changing the measurement frequency based on at least one of a period of time elapsed since a preventative maintenance task has been performed on the processing tool (20) and a period of time elapsed since a particular part in the processing tool (20) has been replaced.

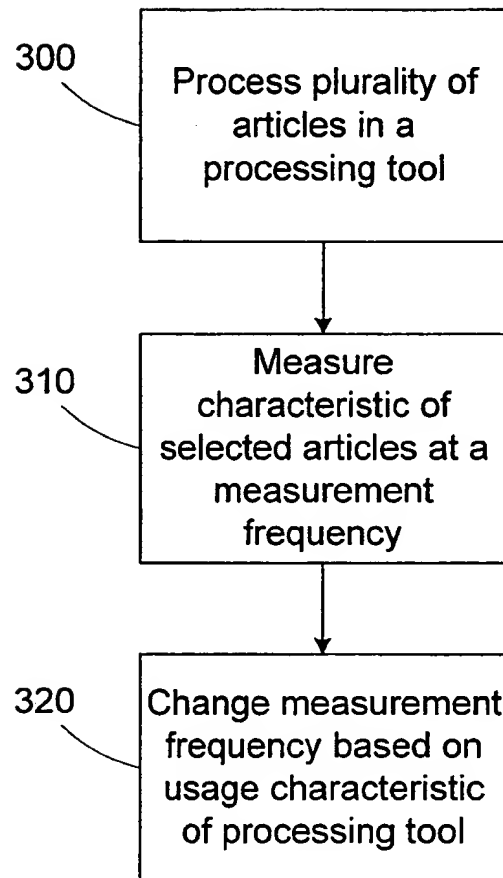
1 / 3

**Figure 1**

2 / 3

**Figure 2****Figure 3**

3 / 3

**Figure 4**

PCT/US 00/24411

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H01L21/66

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category * | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
|------------|--|-----------------------|
| X | US 6 002 989 A (HAMADA TOSHIMITSU ET AL) 14 December 1999 (1999-12-14) column 1, line 22 - line 33 column 3, line 1 - line 15; figures 4,19 | 1-8 |
| A | US 5 274 434 A (OHSHIMA YOSHIMASA ET AL) 28 December 1993 (1993-12-28) column 3, line 13 - line 57 column 4, line 26 - line 53 | 1-8 |

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

*** Special categories of cited documents :**

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

28 November 2000

Date of mailing of the international search report

04/12/2000

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax (+31-70) 340-3016

Authorized officer

Ahlstedt, M

| Patent document cited in search report | | Publication date | Patent family member(s) | | Publication date |
|---|---|---------------------|----------------------------|-----------|---------------------|
| US 6002989 | A | 14-12-1999 | JP | 9269294 A | 14-10-1997 |
| <hr/> | | | | | |
| US 5274434 | A | 28-12-1993 | JP | 3285339 A | 16-12-1991 |
| | | | JP | 2843424 B | 06-01-1999 |
| | | | JP | 4056245 A | 24-02-1992 |
| | | | JP | 4152545 A | 26-05-1992 |
| | | | US | 5463459 A | 31-10-1995 |
| | | | US | 5233191 A | 03-08-1993 |
| | | | KR | 9513433 B | 08-11-1995 |
| <hr/> | | | | | |